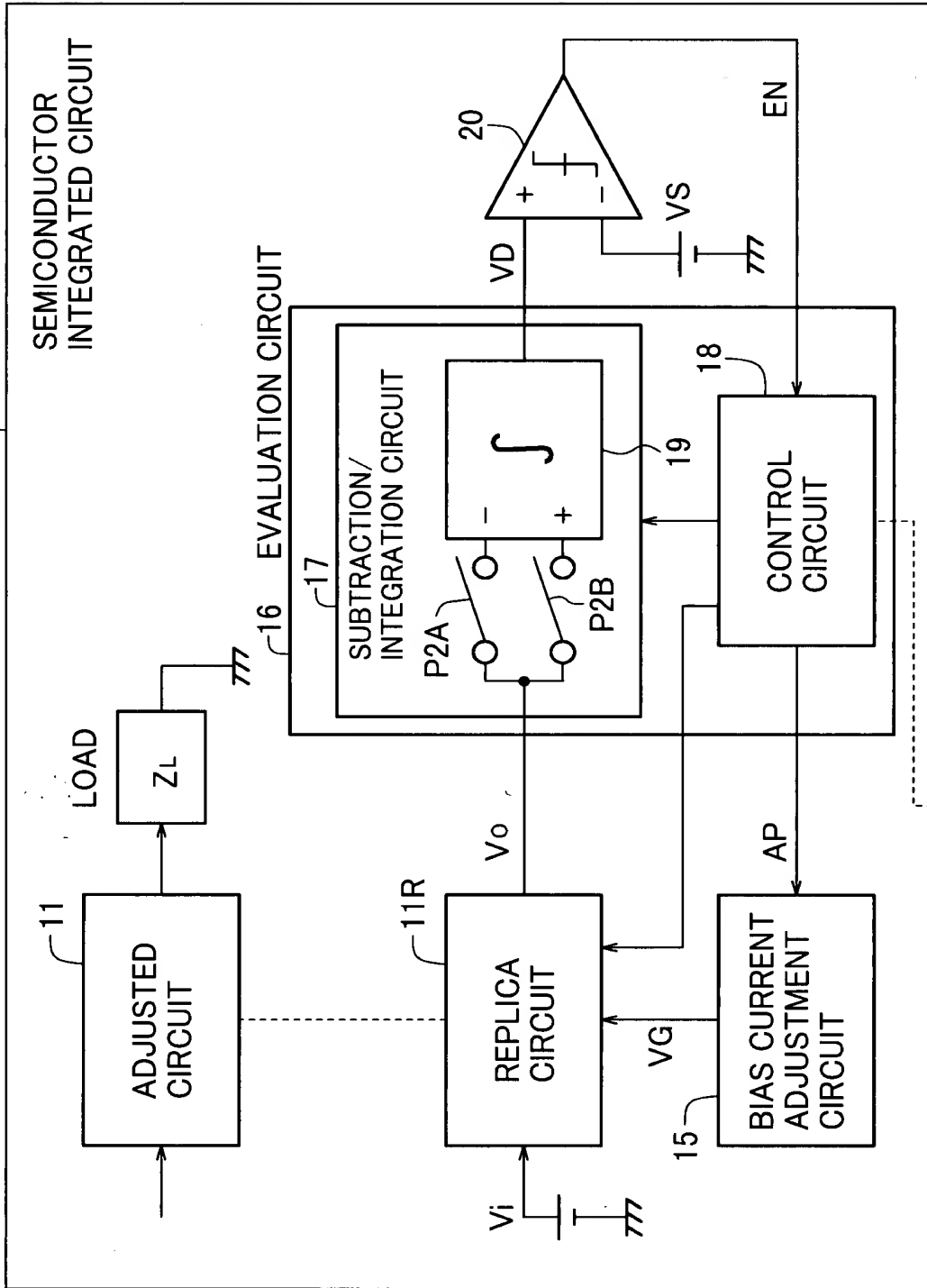


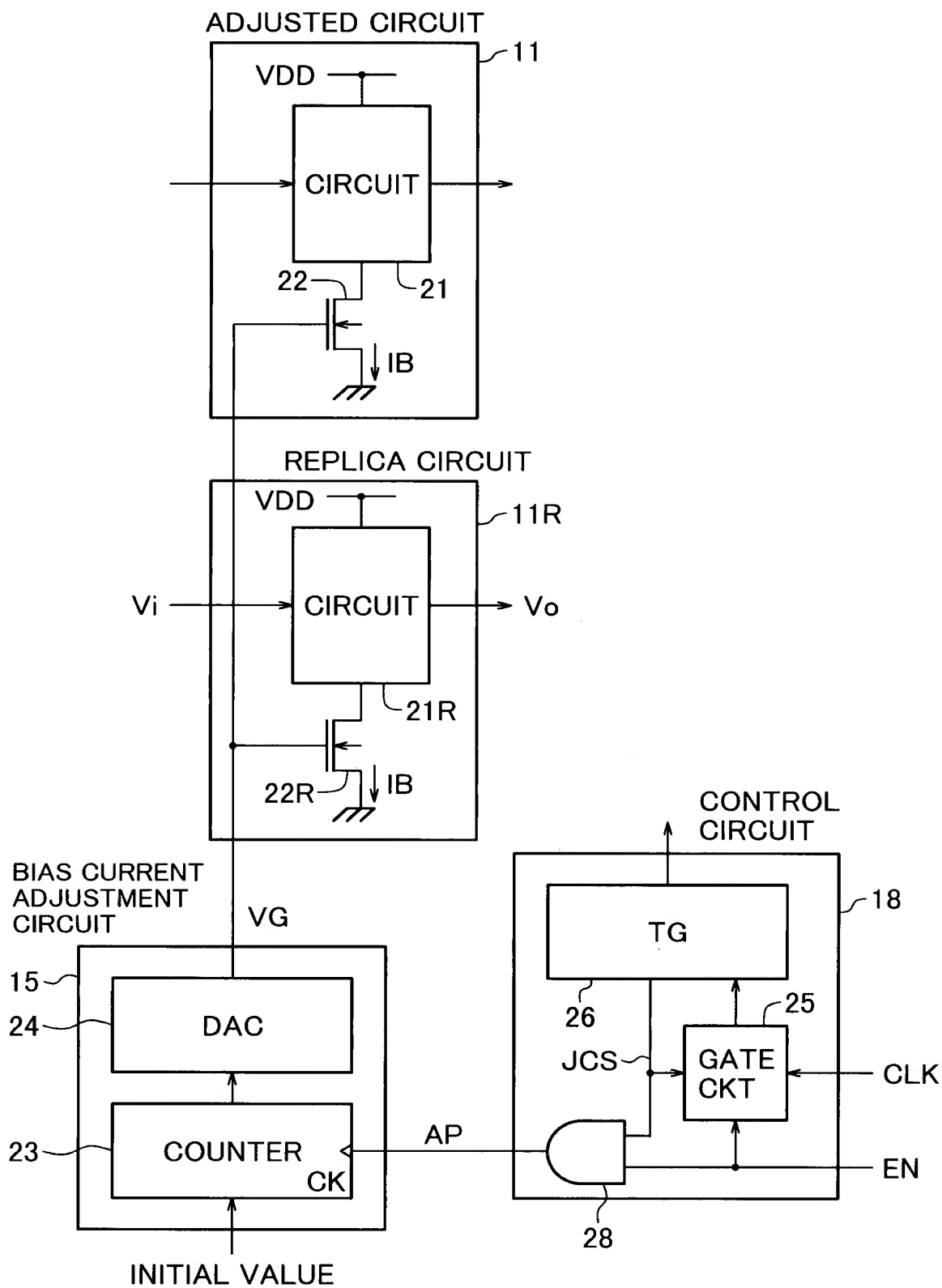
FIG. 1



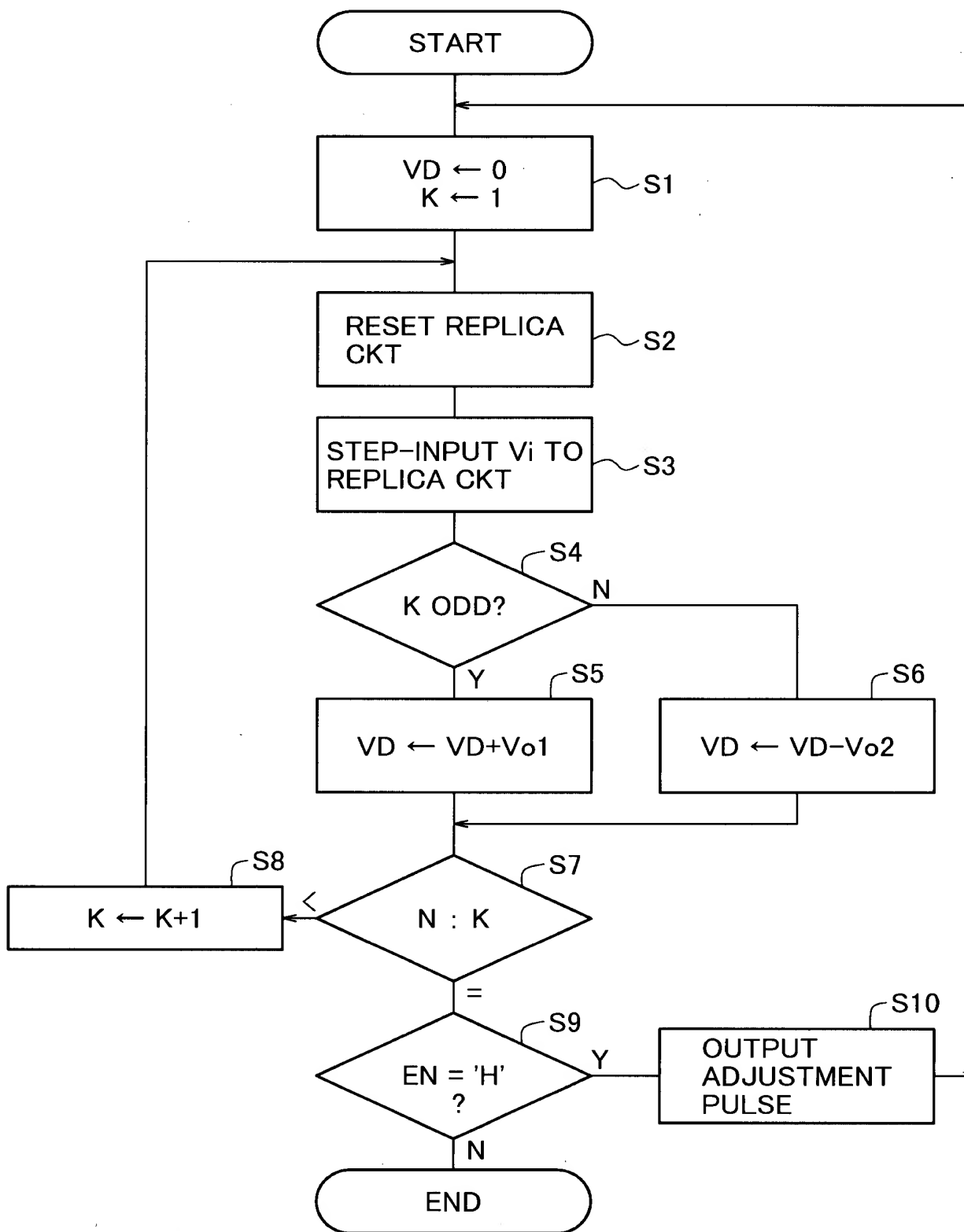
POWER SUPPLY

16 Aug

**FIG.2**



**FIG.3**



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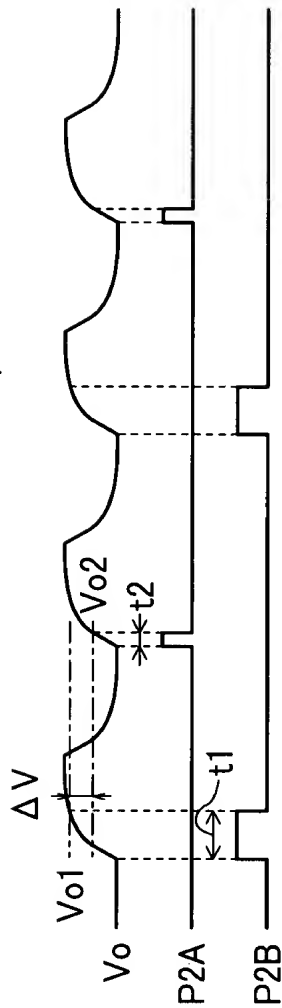


FIG. 4(A)

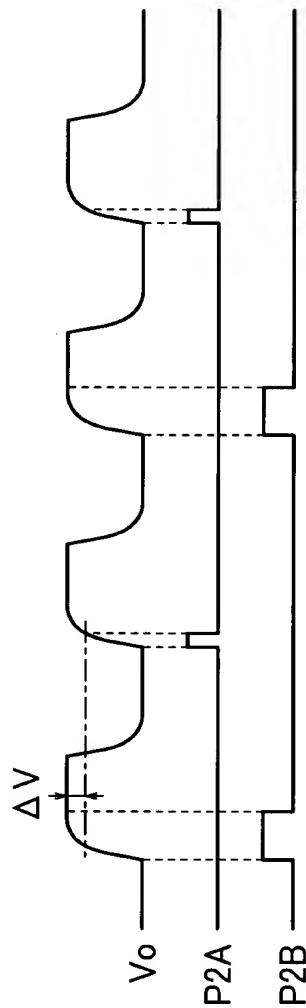


FIG. 4(B)

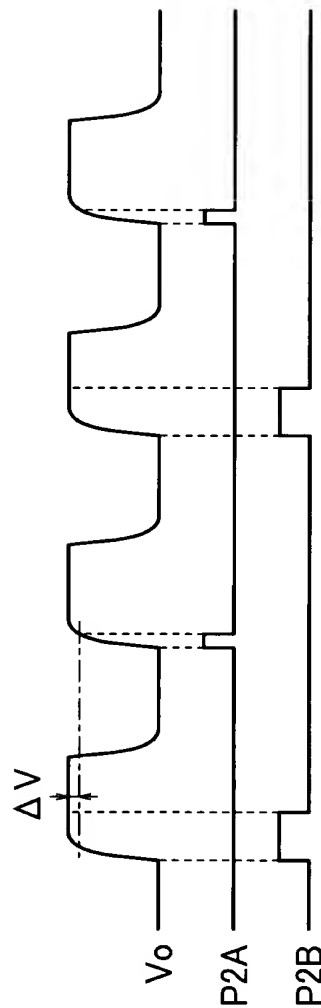
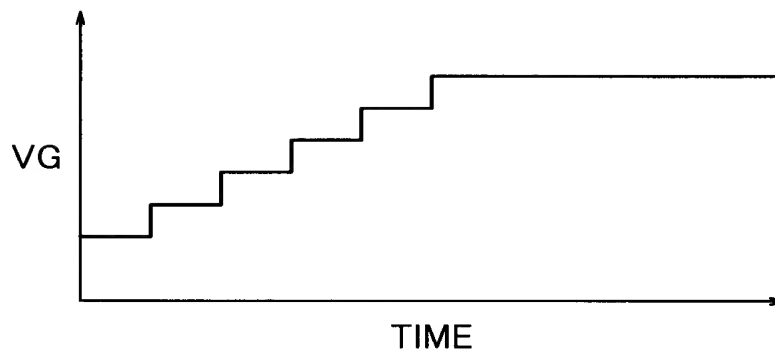
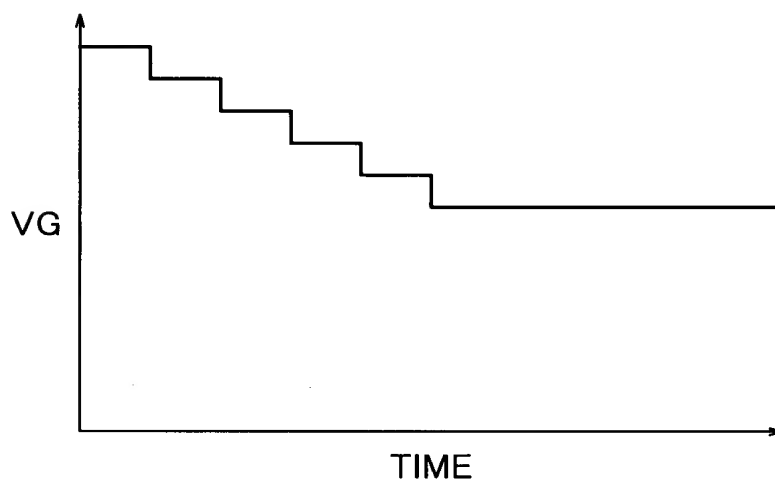


FIG. 4(C)

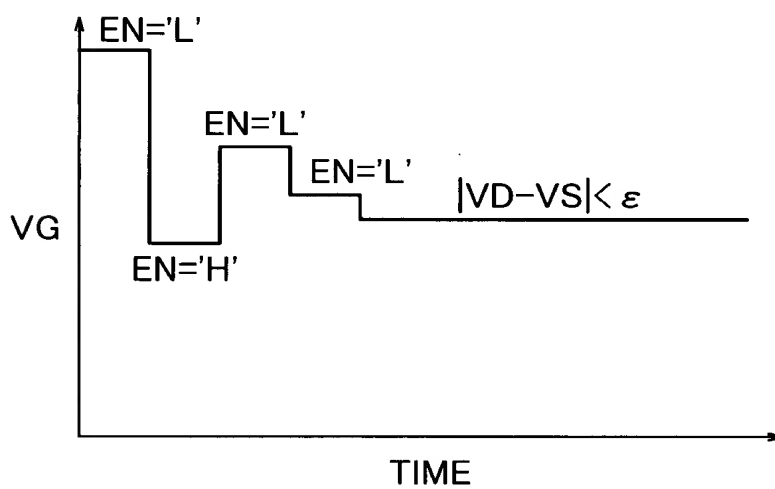
**FIG.5(A)**



**FIG.5(B)**



**FIG.5(C)**



**FIG.6**

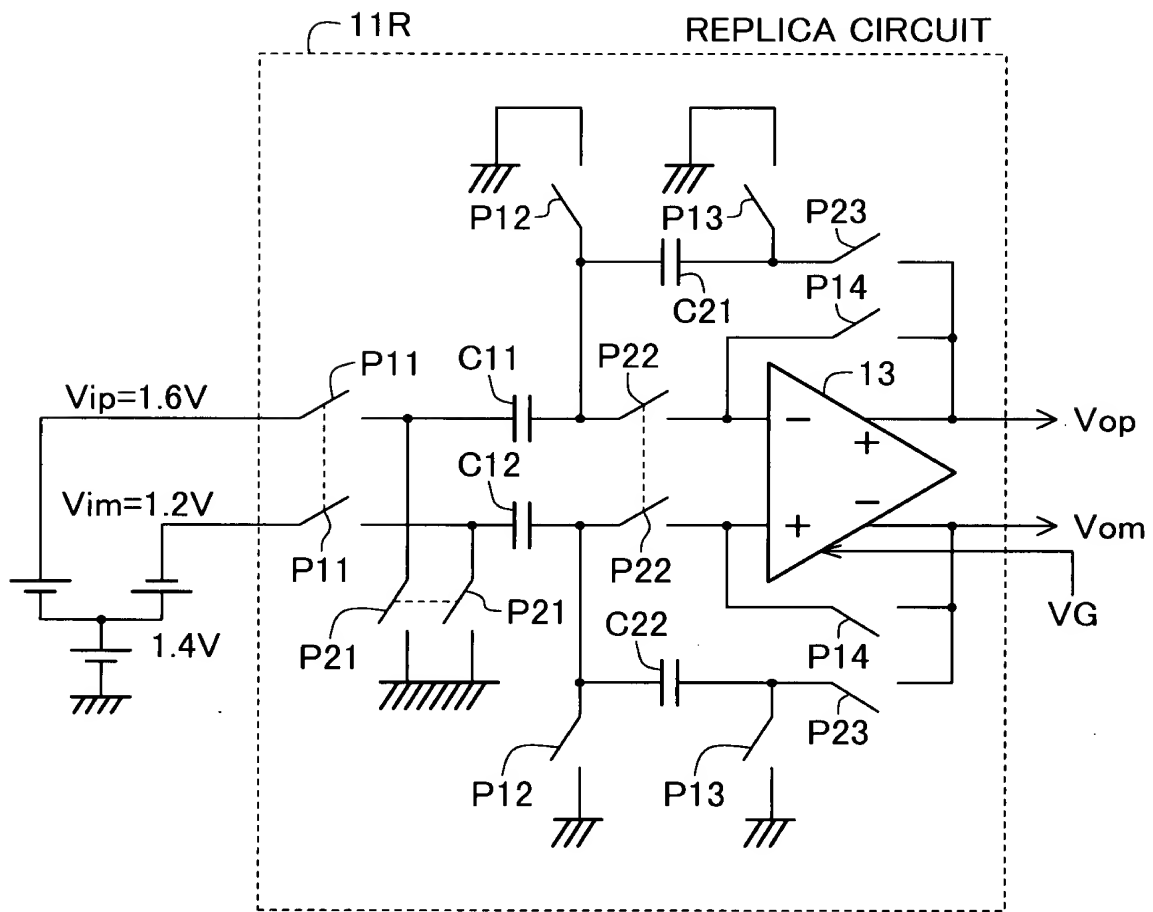


FIG. 7

SUBTRACTION/INTEGRATION CIRCUIT

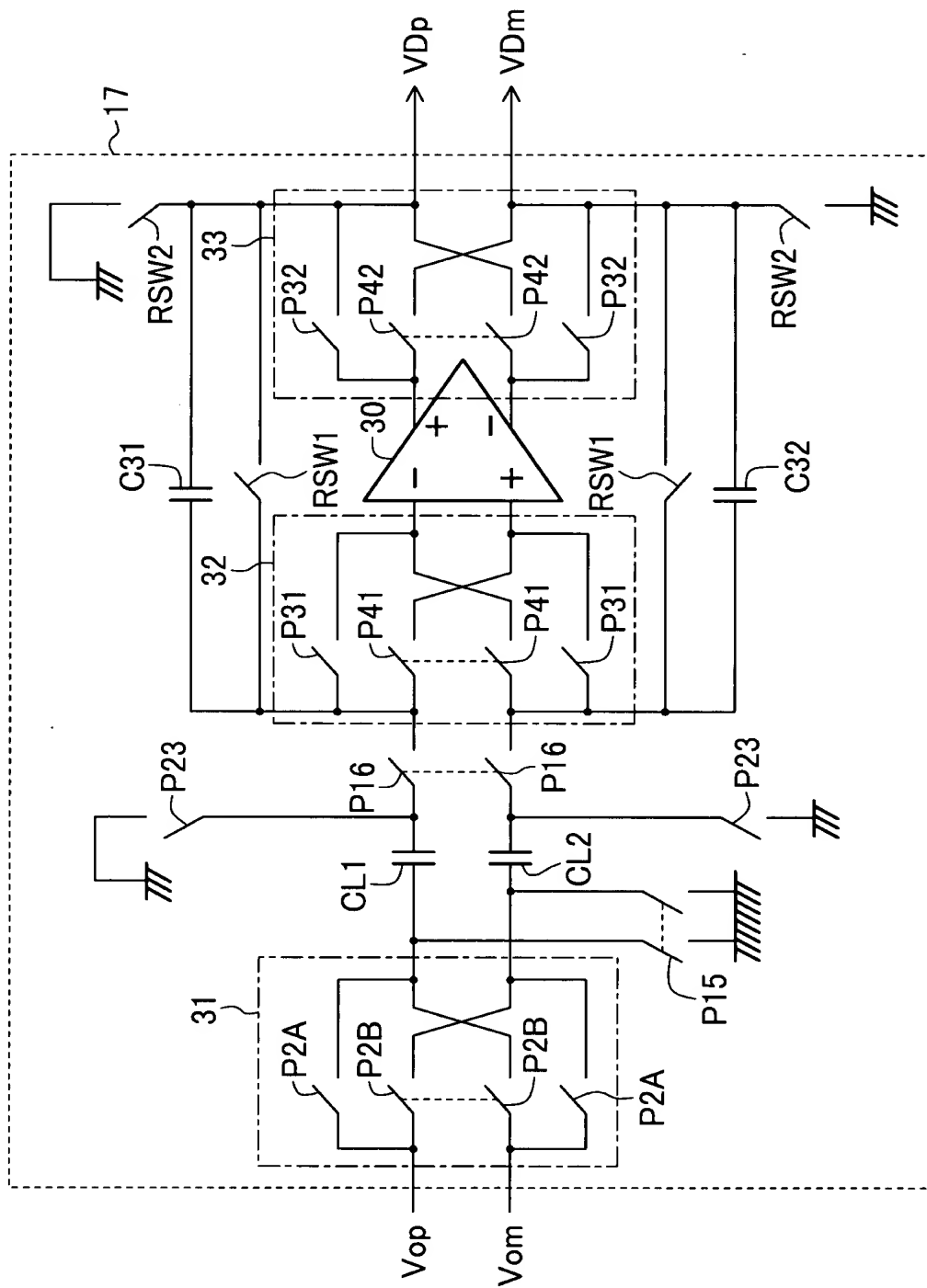
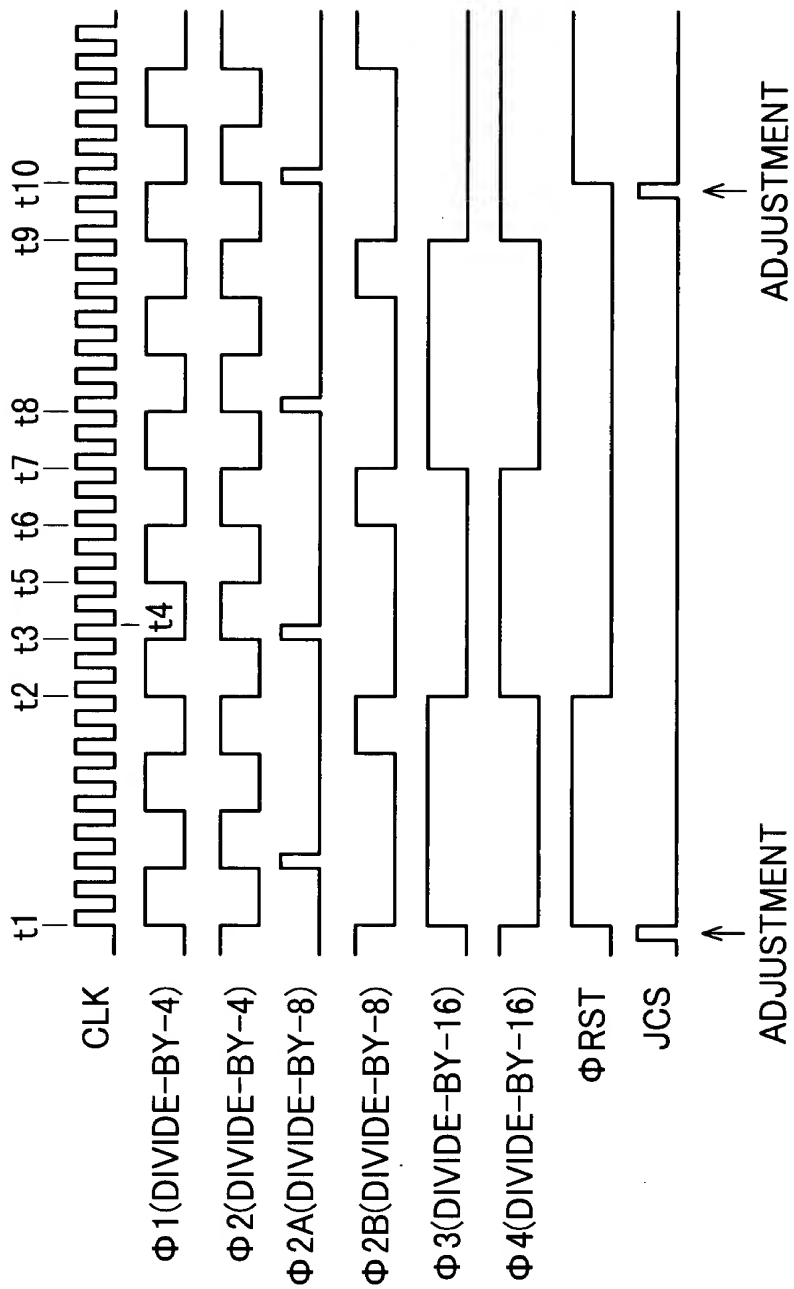


FIG.8





**FIG.9**  
*prior art*

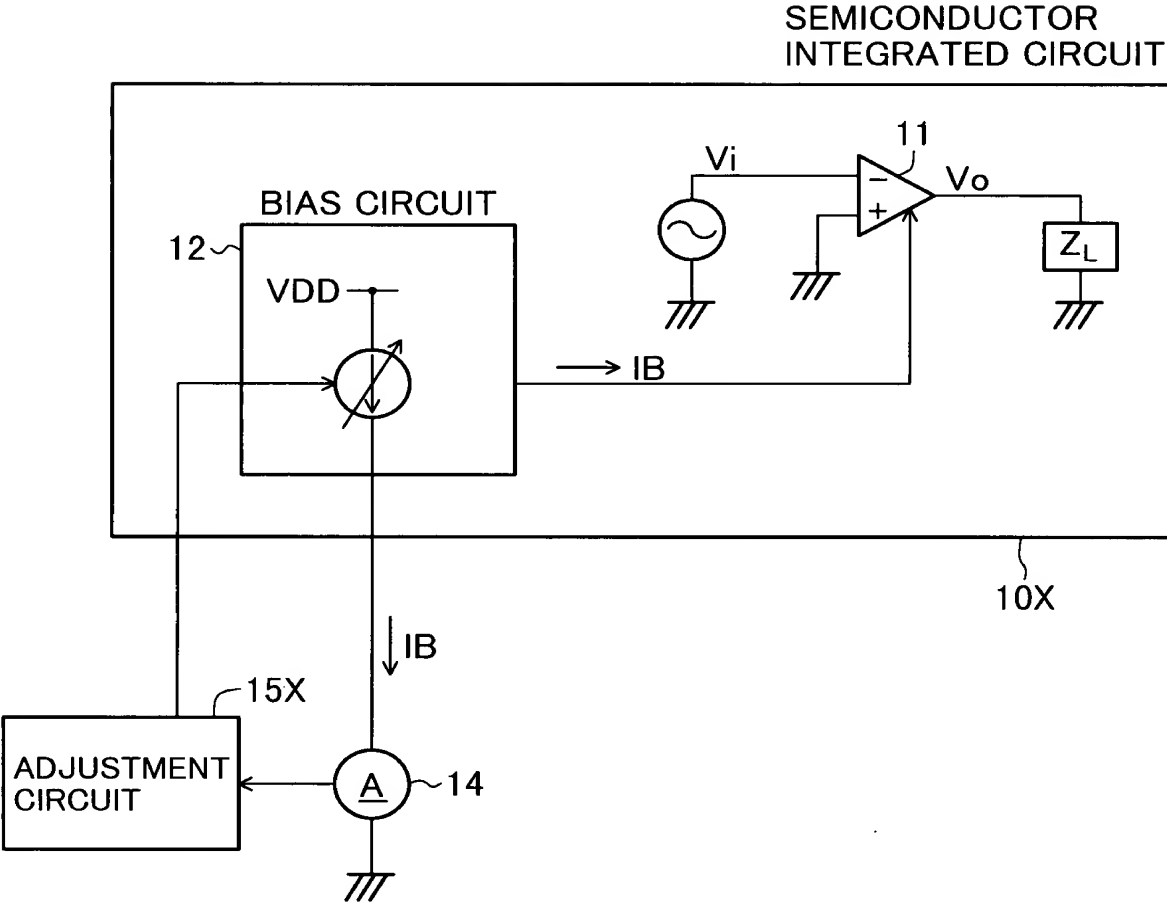
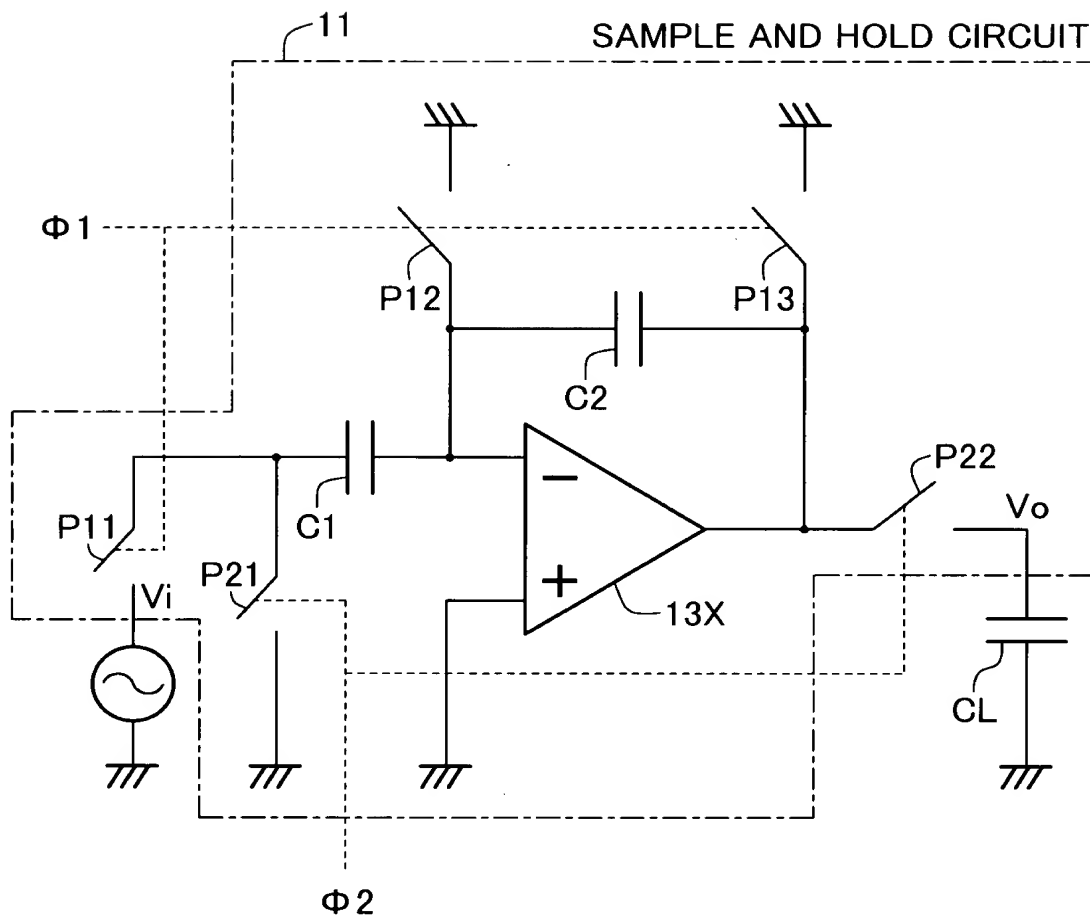
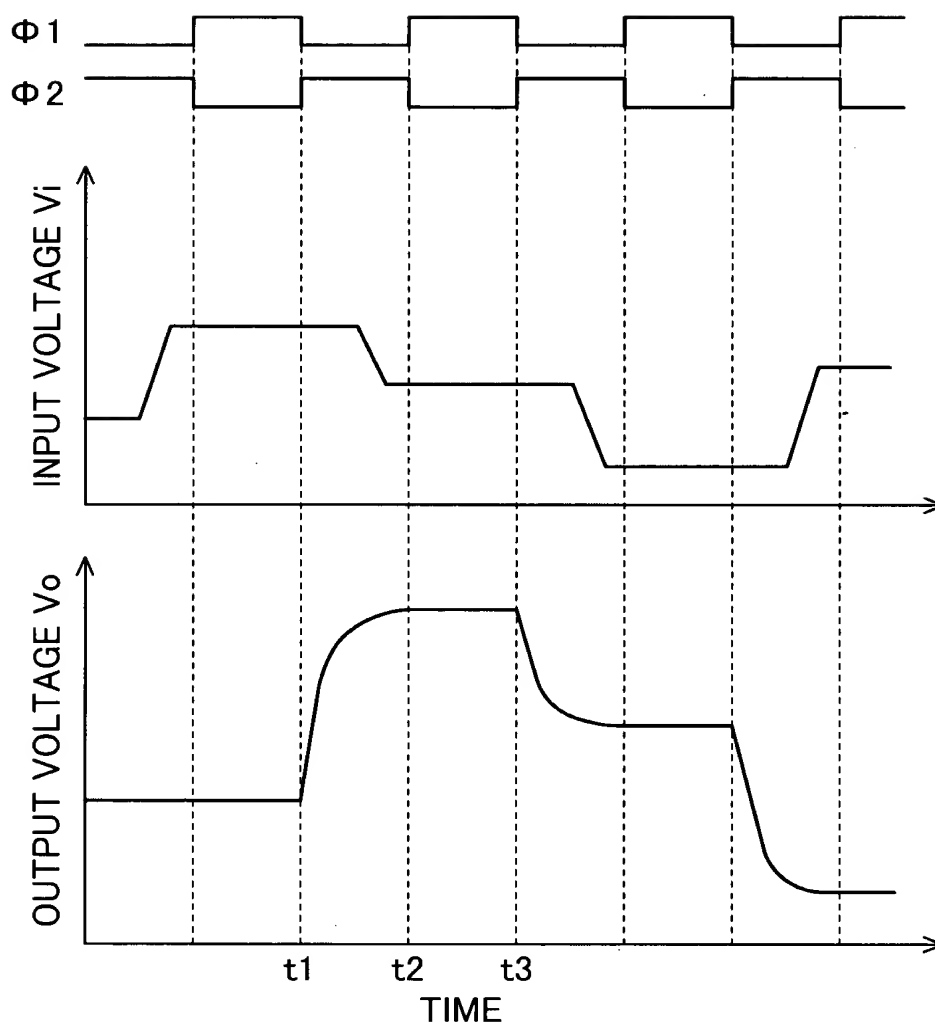


FIG. 9

**FIG.10**  
**prior art**



**FIG. 11**  
*prior art*



**FIG.12**  
**prior art**

